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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): En-Hsing Chen, et al.

Title: NAND MEMORY ARRAY INCORPORATING MULTIPLE SERIES  
SELECTION DEVICES AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,865 Filed: December 5, 2003

Examiner: Nguyen, Van Thu T. Group Art Unit: 2824

Atty. Docket No.: 023-0029 Confirmation No.: 8494

August 24, 2005

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**37 C.F.R. § 1.97(b)**

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- Listing of Pending Applications (N/A page(s), with copy of no application(s)).
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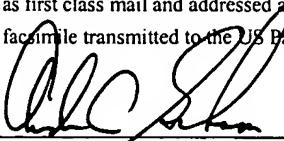
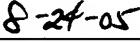
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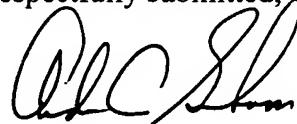
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Respectfully submitted,



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PTO/SB/08A (Substitute for form 1449/PTO)	Attorney Docket No.: 023-0029
	Application No.: 10/729,865
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	First Named Inventor: <i>En-Hsing Chen</i>
<i>O I P E</i> <i>AUG 29 2005</i>	Filing Date: December 5, 2003
	Group Art Unit: 2824
<i>37 CFR 1.10(a)(2)(i)(A)</i> <i>SEARCHED</i> <i>INDEXED</i> <i>MAILED</i> <i>SHEET 11 OF 14</i>	Examiner Name: <i>Nguyen, Van Thu T.</i>
	Date Submitted: August 24, 2005

## U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>2</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
	AA	Patent Abstracts of Japan, Vol. 1998, No. 11	09-30-1998	Arase Kenshirou		
	AB	JP-1998-10149688	06-02-1998	Arase Kenshirou		
<b>Examiner Signature</b>				<b>Date Considered</b>		

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